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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/038,678	01/08/2002	Toyoki Taguchi	217895US-2S DIV	9814
22850	7590	03/10/2004	EXAMINER	
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C. 1940 DUKE STREET ALEXANDRIA, VA 22314			LEUNG, QUYEN PHAN	
			ART UNIT	PAPER NUMBER
			2828	

DATE MAILED: 03/10/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/038,678

Applicant(s)

TAGUCHI ET AL.

Examiner

Quyen P. Leung

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 7-20 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 7-20 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date ____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: ____.

DETAILED ACTION

Drawings

1. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the claim 18 combination of (a) a high frequency signal generating circuit, (b) a high-frequency signal superimposing circuit **and** (c) a modulation signal current generating section for outputting a modulation signal current for adjusting an amount of output light of the semiconductor laser **to the reversal input terminal** of the amplifying circuit must be shown or the feature(s) canceled from the claim(s); the claim 19 combination of a optical system, a detection system, a reproduction system and a modulation signal current generating section for outputting a modulation signal current for adjusting an amount of output light of the semiconductor laser **to the reversal input terminal** of the amplifying circuit; the claim 20 combination of a scanning section, an image formation section, a control signal generating section and a modulation signal current generating section for outputting a modulation signal current for adjusting an amount of output light of the semiconductor laser **to the reversal input terminal** of the amplifying circuit. No new matter should be entered.

Note figure 4 which shows a high frequency signal generating circuit (17) and a high-frequency signal superimposing circuit (20) but does not show a modulation signal current generating section for outputting a modulation signal current for adjusting an amount of output light of the semiconductor laser **to the reversal input terminal** of the amplifying circuit. Instead the modulating signal current (SIG) applied to the resistor

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(Ri). Note figure 12, which shows an image recording apparatus, does not show the modulating signal from (203,213) outputted to the amplifying control circuit (205,206). Instead the modulating signal bypasses the amplifying control circuit altogether.

A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 7-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Taguchi (5,097,473). Taguchi discloses the claimed invention. Note figure 15 which shows a semiconductor laser (12), a light intensity detection element (22), an amplifying circuit (14) a laser drive circuit (16), and a modulation signal current generating section (210) for outputting a modulating signal current (record pulse signal, see col. 11 lines 32-57, below) to the reversal input terminal (-) of the amplifying circuit (14).

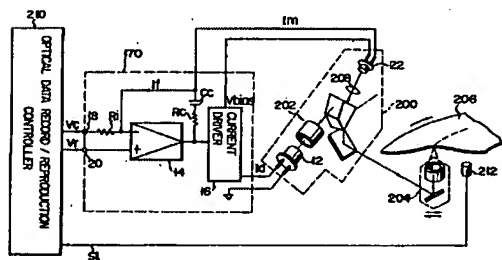


FIG. 15


efficiency of light incident on the photodiode 22. An optimal information record/reproduction control unit 210 is connected to the control apparatus 170 and a temperature sensor 212 located near the surface of the rotating optical disk 206. In accordance with a preselected digital coding scheme, the unit 210 generates a record pulse signal corresponding to information to be recorded and performs waveform equalization. The unit 210 then supplies the control signal Vc to the input terminal 18. The unit 210 optimally updates a reference voltage level Vr of a reference signal in response to an ambient temperature detection signal St from the temperature sensor 212 and supplies the updated level to the input terminal 20 of the control apparatus 170. The control apparatus 170 performs stabilization control of the output light amount of the semiconductor laser 12 by using these signals Vc and Vr.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Quyen P. Leung whose telephone number is (571)272-1943. The examiner can normally be reached on 9-5:30, M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Paul Ip can be reached on (571)272-1941. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


Quyen P. Leung
Primary Examiner
Art Unit 2828

QPL